



$V_{DS} = 1200\text{ V}$ $I_D@25^\circ\text{C} = 105\text{ A}$ $R_{DS(ON)} = 18\text{ m}\Omega$

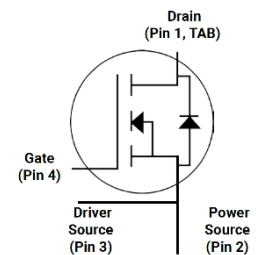
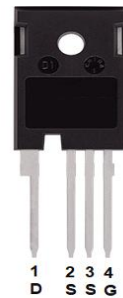
Features

- High Blocking Voltage
- High Frequency Operation
- Low on-resistance
- Fast intrinsic diode with low reverse recovery
- 100% avalanche tested

Benefits

- Higher System Efficiency
- Parallel Device Convenience without thermal runaway
- High Temperature Application
- Hard Switching & Higher Reliability
- Easy to drive

TO-247-4



Applications

- Motor Drives
- Solar / Wind Inverters
- EV Charging Station
- AC/DC converters
- DC/DC converters
- Uninterruptable power supplies

Part Number	Package	Marking
LGE3M18120Q	TO-247-4	LGE3M18120Q

Maximum Ratings ($T_C=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test conditions	Value	Unit
Drain - Source Voltage	V_{DSmax}	$V_{GS}=0\text{V}, I_D=100\mu\text{A}$	1200	V
Gate - Source Voltage (dynamic)	V_{GSmax}	AC ($f>1\text{ Hz}$)	-10 / +25	V
Gate - Source Voltage (static)	V_{GSop}	static	-5 / +20	V
Continuous Drain Current	I_D	$V_{GS} = 20\text{V}, T_C=25^\circ\text{C}$ $V_{GS} = 20\text{V}, T_C=100^\circ\text{C}$	105 74	A
Pulsed Drain Current	$I_{D(pulse)}$	$T_C=25^\circ\text{C}$	220	A
Total power dissipation	P_D	$T_C=25^\circ\text{C}$	428	W
Avalanche Capability	E_{AS}	$V_{DD} = 100\text{V}, V_{GS}=20\text{V}, L=2\text{mH}$	784	mJ
Avalanche Capability	I_{AV}	$V_{DD} = 100\text{V}, V_{GS}=20\text{V}, L=2\text{mH}$	28	A
Operating Junction Temperature	T_J		-55 to 175	$^\circ\text{C}$
Storage Temperature	T_{STG}		-55 to 175	$^\circ\text{C}$

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

Caution: This device is sensitive to electrostatic discharge .Users should follow ESD handling procedures.



Electrical Characteristics (T_C=25°C unless otherwise specified)

Parameter	Symbol	Test conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 100μA	1200			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 20mA	1.9	2.45	3.8	V
		V _{DS} = V _{GS} , I _D = 20mA, T _J = 150°C		1.7		
		V _{DS} = V _{GS} , I _D = 20mA, T _J = 175°C		1.6		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 1200V, V _{GS} = 0V	0	1	50	μA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = 20V, V _{DS} = 0V	0	1	200	nA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = -5V, V _{DS} = 0V	-200	-1	0	nA
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} = 20V, I _D = 50 A		18	26	mΩ
		V _{GS} = 20V, I _D = 50 A, T _J = 150°C		30		
		V _{GS} = 20V, I _D = 50 A, T _J = 175°C		34		
		V _{GS} = 18V, I _D = 50 A		20		
Transconductance	g _{fs}	V _{DS} = 20V, I _D = 50 A,		39		S
		V _{DS} = 20V, I _D = 50 A, T _J = 150°C		34		
		V _{DS} = 20V, I _D = 50 A, T _J = 175°C		33		
Input capacitance	C _{iss}	V _{DS} = 1000V, V _{GS} = 0V f = 1MHz		4800		pF
Output capacitance	C _{oss}			225		
Reverse transfer capacitance	C _{rss}			10		
C _{oss} Stored Energy	E _{oss}			150		
Total gate charge	Q _g	V _{DS} = 800V, V _{GS} = -5V / 20V I _D = 50 A,		235		nC
Gate-source charge	Q _{gs}			62		
Gate-drain charge	Q _{gd}			75		
Internal gate input resistance	R _{g(int)}	f = 1MHz, I _D = 0A		3.6		Ω
Turn-On Switching Energy	E _{ON}	V _{DS} = 800 V, V _{GS} = -5V/20V, I _D = 50A, R _{G(ext)} = 2Ω, L = 200μH		400		μJ
Turn-Off Switching Energy	E _{OFF}			135		
Turn-On Delay Time	t _{d(on)}			15		ns
Rise Time	t _r			22		
Turn-Off Delay Time	t _{d(off)}			44		
Fall Time	t _f			11		

Caution: This device is sensitive to electrostatic discharge .Users should follow ESD handing procedures.



Reverse Diode Characteristics ($T_C=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Diode Forward Voltage	V_{SD}	$V_{GS} = -5V, I_{SD} = 25A,$		4.3		V
		$V_{GS} = -5V, I_{SD} = 25A,$ $T_J = 150^\circ\text{C}$		3.9		
		$V_{GS} = -5V, I_{SD} = 25A,$ $T_J = 175^\circ\text{C}$		3.8		
Continuous Diode Forward Current	I_S	$V_{GS} = -5V$			91	A
Reverse Recovery time	t_{rr}	$V_{GS} = -5V, I_{SD} = 50A,$ $V_R = 800V, dif/dt = 3700 A/\mu s$		30		ns
Reverse Recovery Charge	Q_{rr}			970		nC
Peak Reverse Recovery Current	I_{rrm}			53		A

Thermal Characteristics

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Thermal Resistance (per device)	$R_{th(j-c)}$	junction-case		0.27	0.35	$^\circ\text{C}/\text{W}$

Caution: This device is sensitive to electrostatic discharge .Users should follow ESD handing procedures.



Typical Performance

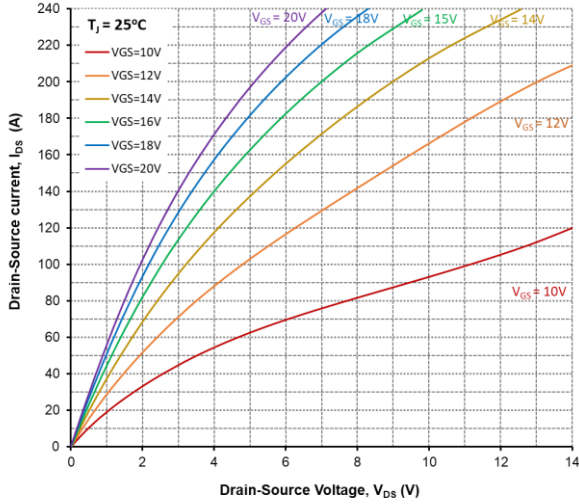


Figure 1. Output Characteristics, $T_J = 25^\circ\text{C}$

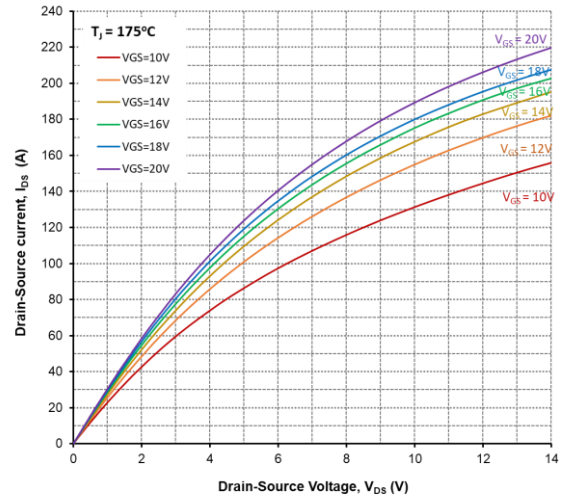


Figure 2. Output Characteristics, $T_J = 175^\circ\text{C}$

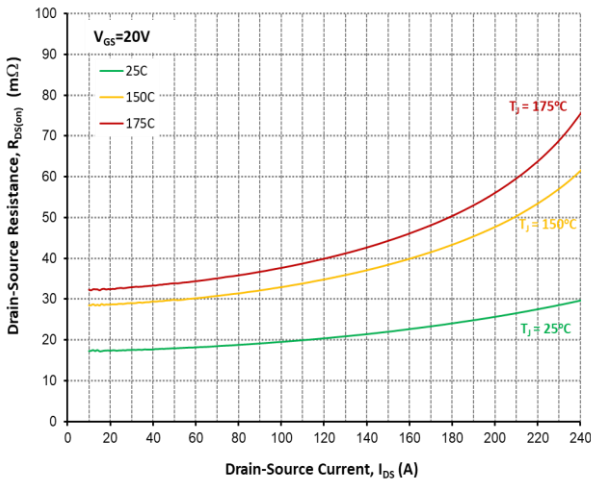


Figure 3. On-Resistance vs. Drain Current For Various Temperatures

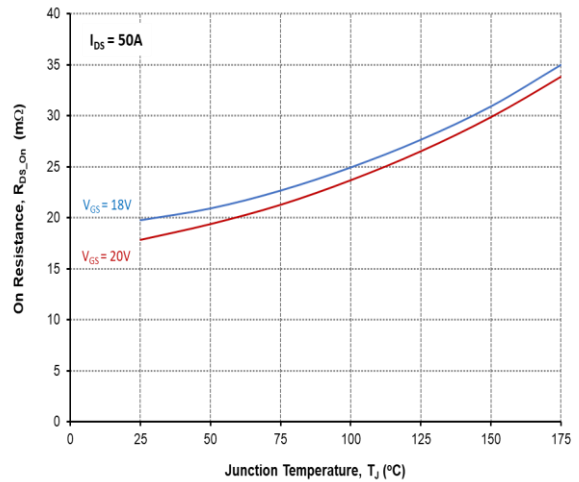


Figure 4. On-Resistance vs. Temperature

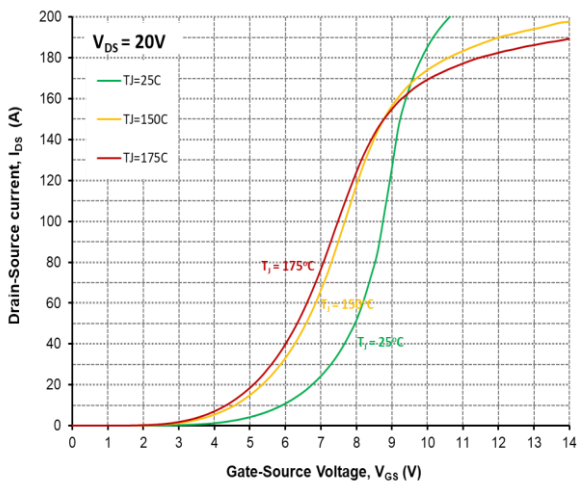


Figure 5. Transfer Characteristic For Various Junction Temperatures

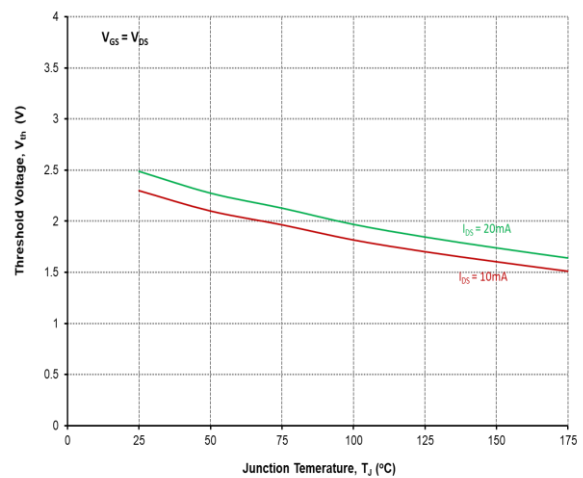


Figure 6. Threshold Voltage vs. Temperature

Caution: This device is sensitive to electrostatic discharge .Users should follow ESD handling procedures.



Typical Performance

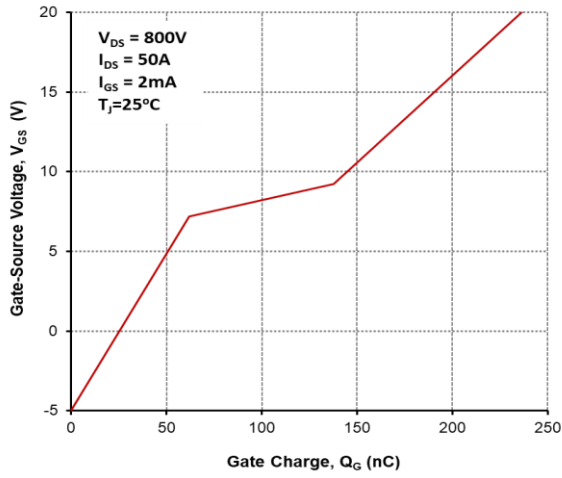


Figure 7. Gate Charge Characteristics

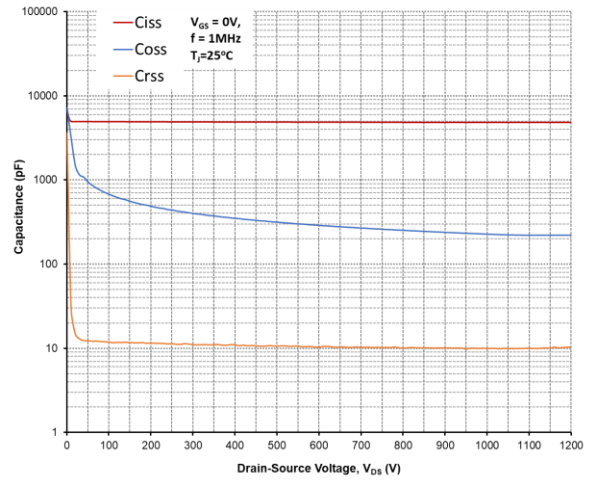


Figure 8. Capacitances vs. Drain-Source Voltage (0-1200V)

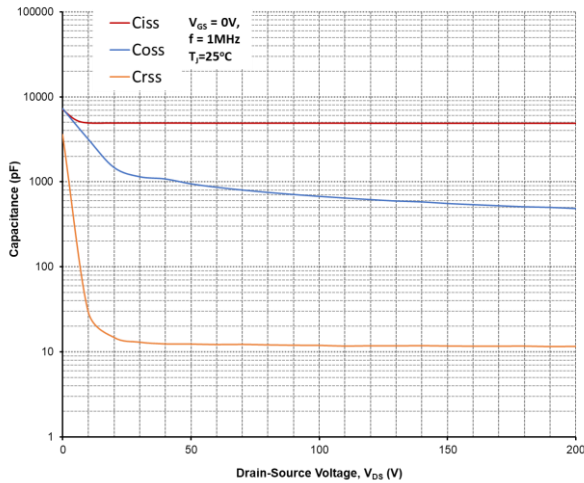


Figure 9. Capacitances vs. Drain-Source Voltage (0-200V)

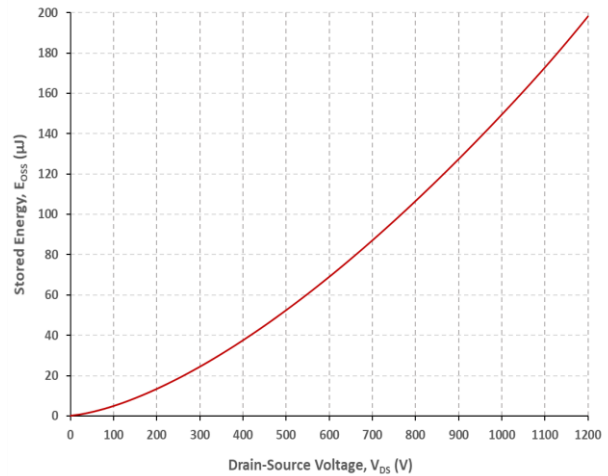


Figure 10. Output Capacitor Stored Energy

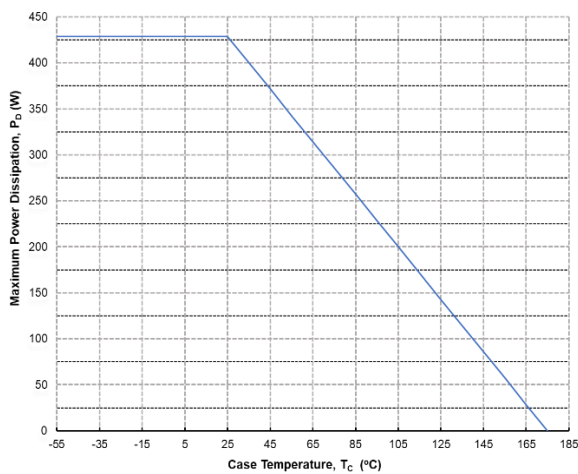


Figure 11. Maximum Power Dissipation Derating vs. Case Temperature

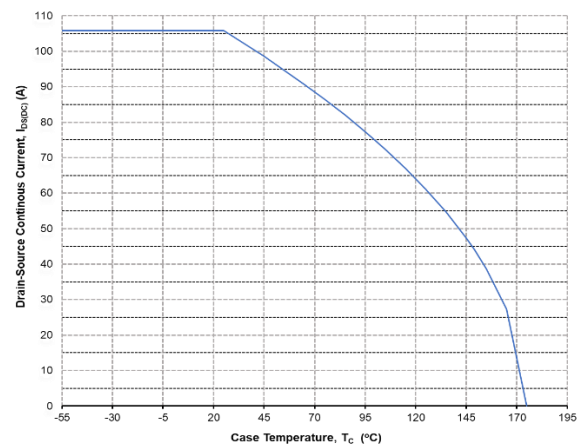


Figure 12. Continuous Drain Current Derating vs. Case Temperature

Caution: This device is sensitive to electrostatic discharge .Users should follow ESD handing procedures.



Typical Performance

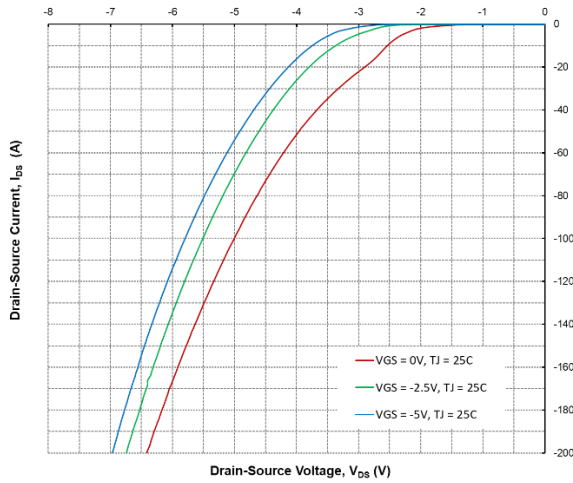


Figure 13. Body Diode Characteristics @ 25°C

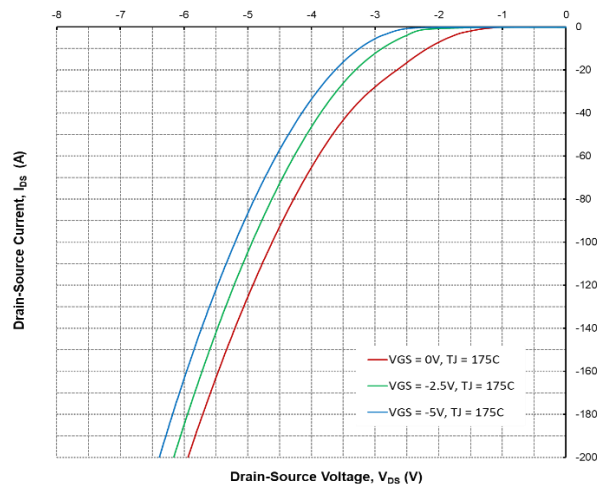


Figure 14. Body Diode Characteristics @ 175°C

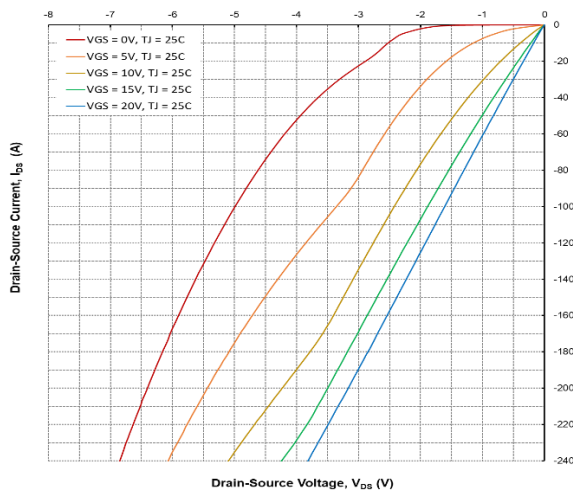


Figure 15. 3rd Quadrant Characteristics @ 25°C

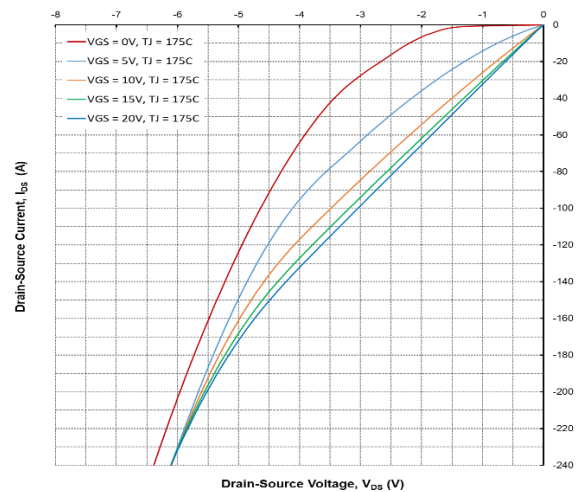


Figure 16. 3rd Quadrant Characteristics @ 175°C

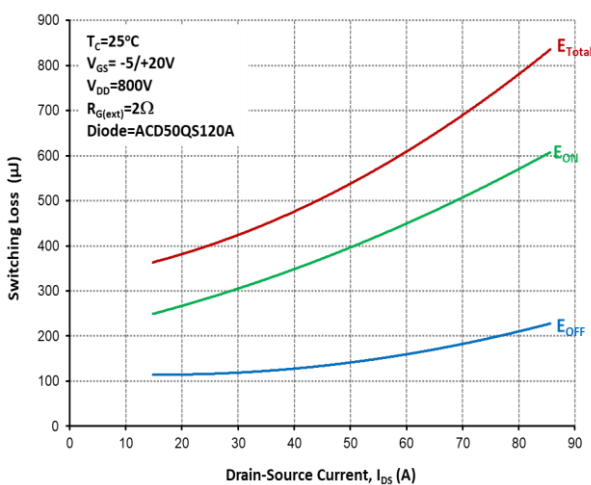


Figure 17. Clamped Inductive Switching Energy vs. Drain Current

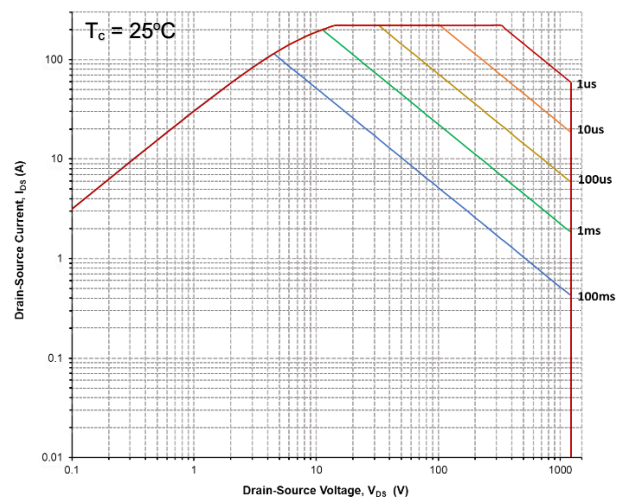


Figure 18. Safe Operating Area

Caution: This device is sensitive to electrostatic discharge .Users should follow ESD handling procedures.

Typical Performance

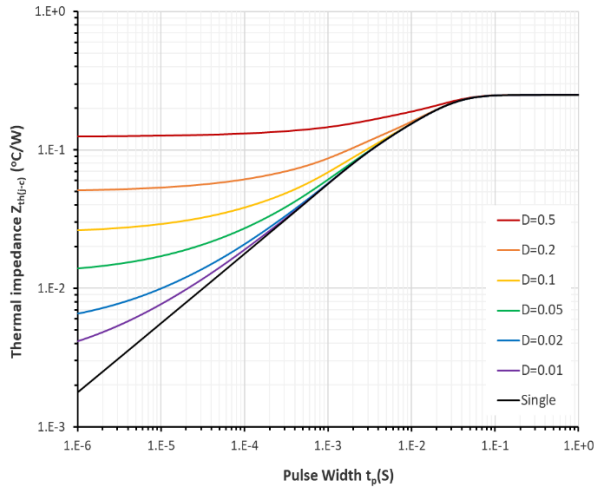
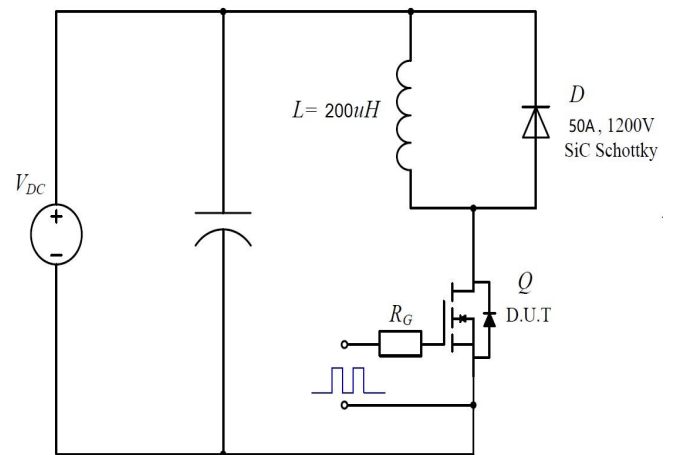
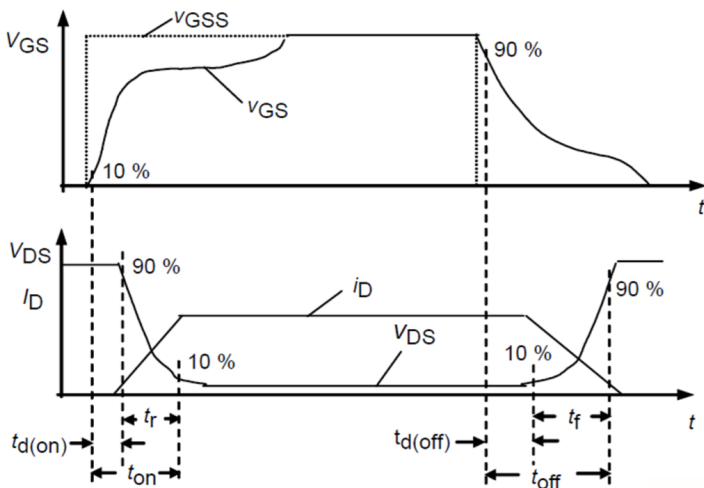


Figure 19. Transient Thermal Impedance (Junction – Case)

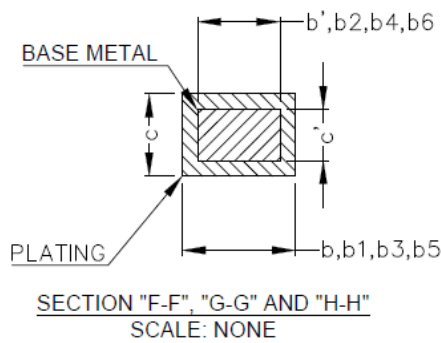
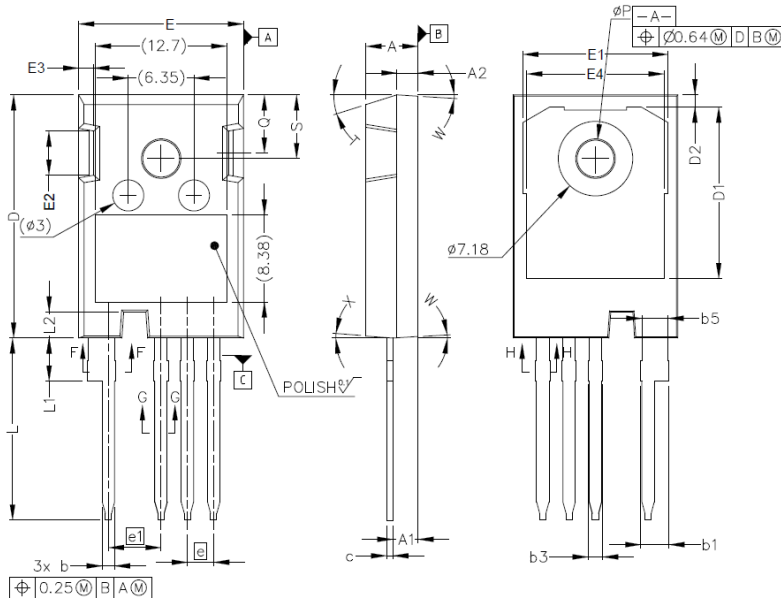
Switching Times Definition and Test Circuit



Caution: This device is sensitive to electrostatic discharge .Users should follow ESD handling procedures.

Package Dimensions

(TO-247-4 Package)



SYMBOL	MILLIMETERS	
	MIN	MAX
A	4.83	5.21
A1	2.29	2.54
A2	1.91	2.16
b'	1.07	1.28
b	1.07	1.33
b1	2.39	2.94
b2	2.39	2.84
b3	1.07	1.60
b4	1.07	1.50
b5	2.39	2.69
b6	2.39	2.64
c'	0.55	0.65
c	0.55	0.68
D	23.30	23.60
D1	16.25	17.65
D2	0.95	1.25
E	15.75	16.13
E1	13.10	14.15
E2	3.68	5.10
E3	1.00	1.90
E4	12.38	13.43
e	2.54 BSC	
e1	5.08 BSC	
N	4	
L	17.31	17.82
L1	3.97	4.37
L2	2.35	2.65
øP	3.51	3.65
Q	5.49	6.00
S	6.04	6.30
T	17.5° REF.	
W	3.5° REF.	
X	4° REF.	

NOTE :
 1. ALL METAL SURFACES: TIN PLATED, EXCEPT AREA OF CUT
 2. DIMENSIONING & TOLERANCEING CONFIRM TO ASME Y14.5M-1994.
 3. ALL DIMENSIONS ARE IN MILLIMETERS. ANGLES ARE IN DEGREES.

Package	Packing	Box Size LxWxH(mm)	Quantity(pcs/box)	Carton Size LxWxH(mm)	Quantity(pcs/carton)
TO-247	30pcs/Tube	570x155x50	450	580x340x125	1800

Caution: This device is sensitive to electrostatic discharge .Users should follow ESD handling procedures.