



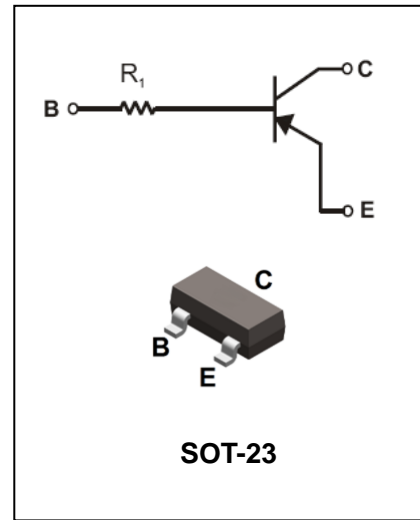
FEATURES

- Epitaxial planar die construction
- Complementary NPN types available(DTC)
- Built-in biasing resistor, R₁ only
- Also available in lead free version

APPLICATIONS

- The PNP style digital transistor

ORDERING INFORMATION



Type No.	Marking	Package Code
DTA114TCA	94	SOT-23
DTA143TCA	93	SOT-23
DTA144TCA	96	SOT-23

MAXIMUM RATING @ T_A = 25°C unless otherwise specified

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	-50	V
V _{CEO}	Collector-Emitter Voltage	-50	V
V _{EBO}	Emitter-Base Voltage	-5	V
I _{C(Max.)}	Collector Current	-100	mA
P _D	Power Dissipation	200	mW
R _{θJA}	Thermal Resistance, Junction to Ambient Air *1	409	°C/W
R _{θJC}	Thermal Resistance, Junction to Case *1	225	°C/W
R _{θJL}	Thermal Resistance, Junction to Lead *1	197	°C/W
T _{J, T_{STG}}	Operating and Storage and Temperature Range	-55 to +150	°C

Note 1: The data tested by surface mounted on a 15mm * 15mm * 1mm FR4-epoxy P.C.B



ELECTRICAL CHARACTERISTICS @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Test conditions	MIN	TYP	MAX	UNIT
Collector-Base breakdown Voltage	BV_{CBO}	$I_C = -50\mu\text{A}$	-50			V
Collector-Emitter breakdown Voltage	BV_{CEO}	$I_C = -1\text{mA}$	-50			V
Emitter-Base breakdown Voltage	BV_{EBO}	$I_E = -50\mu\text{A}$	-5			V
Collector cutoff Current	I_{CBO}	$V_{CB} = -50\text{V}$	-	-	-0.5	μA
Emitter cutoff Current	I_{EBO}	$V_{EB} = -4\text{V}$	-	-	-0.5	μA
Collector-Emitter saturation voltage DTA114TCA DTA143TCA DTA144TCA	$V_{CE(sat)}$	$I_C/I_B = -1\text{mA}/-0.1\text{mA}$ $I_C/I_B = -2.5\text{mA}/-0.25\text{mA}$ $I_C/I_B = -2.5\text{mA}/-0.25\text{mA}$			-0.3	V
DC Current Gain	h_{FE}	$I_C = -1\text{mA}, V_{CE} = -5\text{V}$	100	250	600	
Input Resistor(R_1) DTA114TCA DTA143TCA DTA144TCA	R_1		7 3.19 31.9	10 4.7 47	13 6.11 61.1	K Ω
Input Resistor(R_1)Tolerance	ΔR_1		-30		+30	%
Gain-Bandwidth Product	f_T	$V_{CE} = -10\text{V}, I_E = -5\text{mA},$ $f = 100\text{MHz}$	-	250	-	MHz



TYPICAL CHARACTERISTICS @ Ta=25°C unless otherwise specified

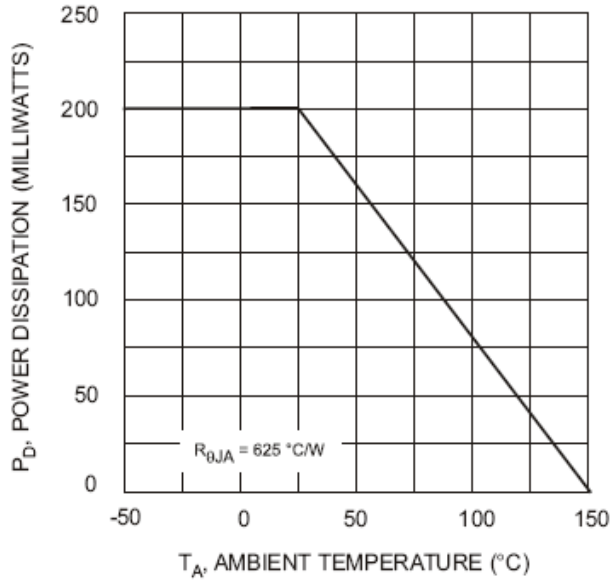


Fig. 1 Derating Curve

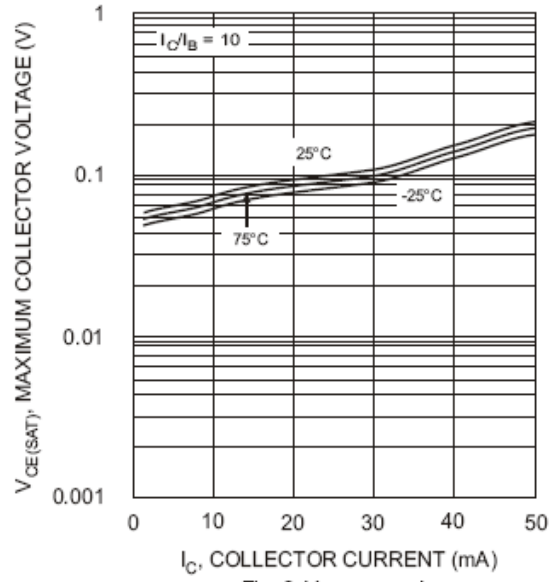


Fig. 2 V_{CE(SAT)} vs. I_C

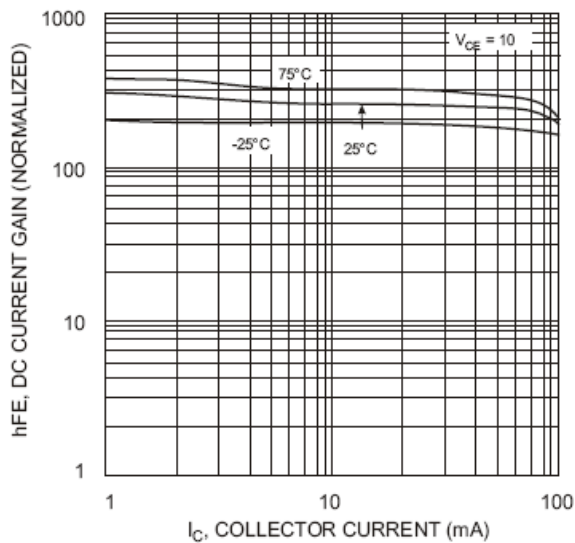


Fig. 3 DC Current Gain

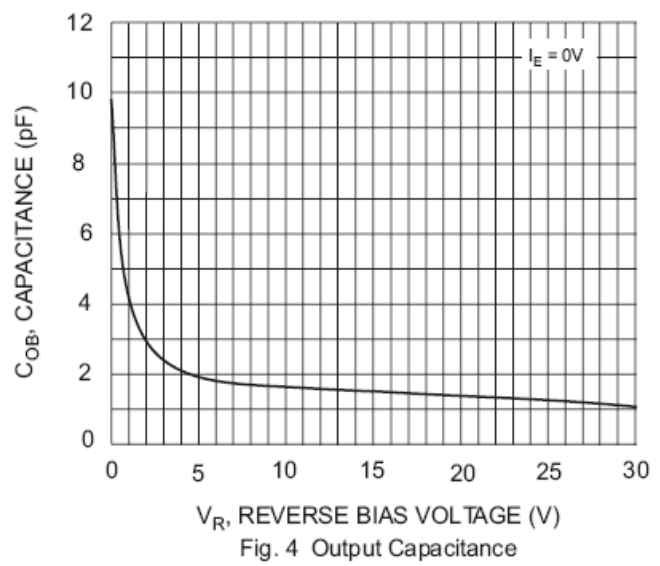


Fig. 4 Output Capacitance

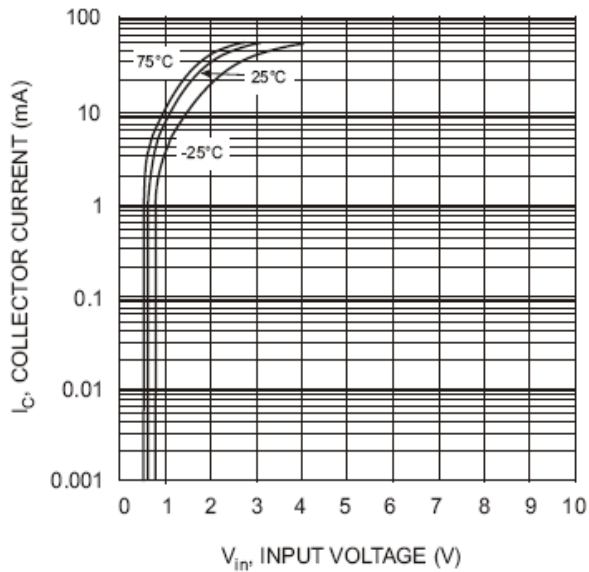


Fig. 5 Collector Current Vs. Input Voltage

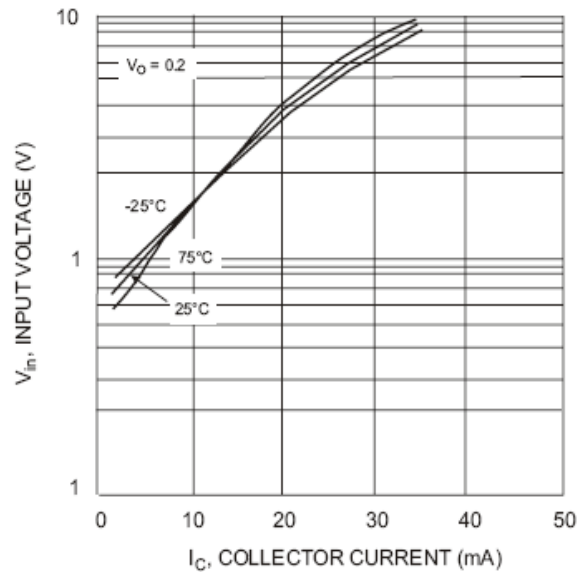


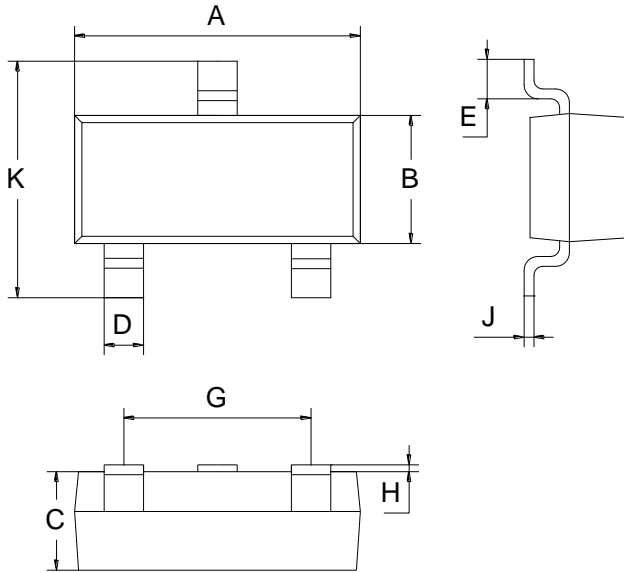
Fig. 6 Input Voltage vs. Collector Current



PACKAGE OUTLINE

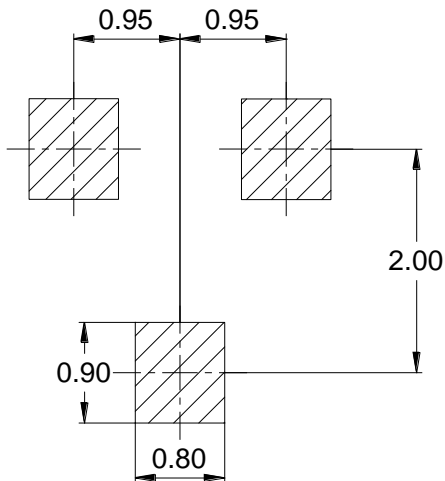
Plastic surface mounted package

SOT-23



SOT-23		
Dim	Min	Max
A	2.70	3.10
B	1.10	1.50
C	0.90	1.10
D	0.30	0.50
E	0.35	0.48
G	1.80	2.00
H	0.02	0.10
J	0.05	0.15
K	2.20	2.60
All Dimensions in mm		

SOLDERING FOOTPRINT



Unit: mm

PACKAGE INFORMATION

Device	Package	Shipping
DTA114TCA/143TCA/144TCA	SOT-23	3000 pcs / Tape & Reel