



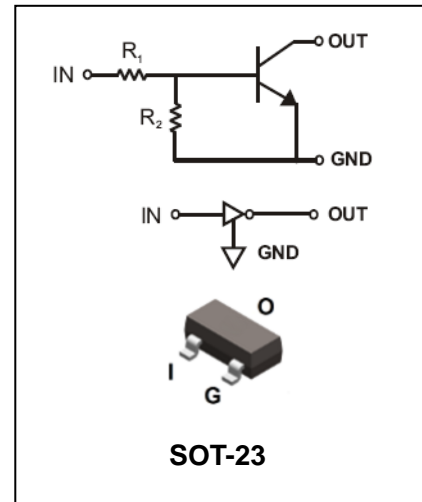
### FEATURES

- Epitaxial planar die construction.
- Complementary PNP types available(DTA).
- Built-in biasing resistors,  $R_1=R_2$ .
- Also available in lead free version.

### APPLICATIONS

- The NPN style digital transistor.

### ORDERING INFORMATION



Type No.	Marking	Package Code
DTC114ECA	24	SOT-23
DTC124ECA	25	SOT-23
DTC143ECA	23	SOT-23
DTC144ECA	26	SOT-23
DTC115ECA	29	SOT-23

### MAXIMUM RATING @ Ta=25°C unless otherwise specified

Symbol	Parameter	Value	Units	
$V_{CC}$	Supply Voltage	50	V	
$V_{IN}$	Input Voltage	DTC114ECA DTC124ECA DTC143ECA DTC144ECA DTC115ECA	-10 to+40 -10 to+40 -10 to+30 -10 to+40 -10 to +40	V
$I_O$	Output Current	DTC114ECA DTC124ECA DTC143ECA DTC144ECA DTC115ECA	50 30 100 100 100	mA
$I_C(\text{Max.})$	Output current	ALL	100	mA
$P_D$	Power Dissipation		200	mW
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient Air *1		409	°C/W
$R_{\theta JC}$	Thermal Resistance, Junction to Case *1		225	°C/W
$R_{\theta JL}$	Thermal Resistance, Junction to Lead *1		197	°C/W
$T_J, T_{STG}$	Operating and Storage and Temperature Range		-55 to +150	°C



### ELECTRICAL CHARACTERISTICS @ Ta=25°C unless otherwise specified

Parameter	Symbol	Test conditions	MIN	TYP	MAX	UNIT
Input Voltage	$V_{I(off)}$	$V_{CC}=5V, I_o=100\mu A$	0.5	-	-	V
Input Voltage	$V_{I(on)}$	DTC114ECA $V_o=0.3V, I_o=10mA$	-	-	3	
		DTC124ECA $V_o=0.2V, I_o=5mA$				
		DTC143ECA $V_o=0.3V, I_o=20mA$				
		DTC144ECA $V_o=0.3V, I_o=2mA$				
		DTC115ECA $V_o=0.3V, I_o=1mA$				
Output Voltage	$V_{O(on)}$	DTC114ECA $I_o/I_i=10mA/0.5mA$	-	-	0.3	V
		DTC124ECA $I_o/I_i=10mA/0.5mA$				
		DTC143ECA $I_o/I_i=10mA/0.5mA$				
		DTC144ECA $I_o/I_i=10mA/0.5mA$				
		DTC115ECA $I_o/I_i=5mA/0.25mA$				
Input Current	$I_i$	$V_i=5V$	-	-	0.88	mA
					0.36	
					1.8	
					0.18	
					0.15	
Output Current	$I_{O(off)}$	$V_{CC}=50V, V_i=0V$	-	-	0.5	$\mu A$
DC Current Gain	$G_I$	$V_o=5V, I_o=5mA$	30	-	-	-
			56			
			20			
			68			
			82			
Input Resistor	$R_1(R_2)$		7	10	13	k $\Omega$
			15.4	22	28.6	
			3.29	4.7	6.11	
			32.9	47	61.1	
			70	100	130	
Resistance Ratio	$R_2/R_1$	-	0.8	1	1.2	
Gain-Bandwidth Product	$f_T$	$V_{CE}=10V, I_E=-5mA,$ $f=100MHz$	-	250	-	MHz

Note 1: The data tested by surface mounted on a 15mm \* 15mm \* 1mm FR4-epoxy P.C.B



### TYPICAL CHARACTERISTICS @ Ta=25°C unless otherwise specified

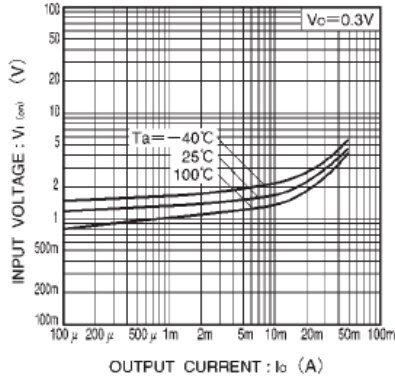


Fig.1 Input voltage vs. output current (ON characteristics)

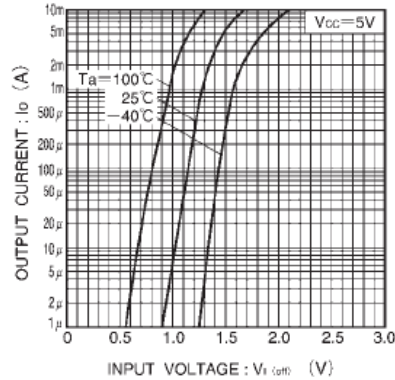


Fig.2 Output current vs. input voltage (OFF characteristics)

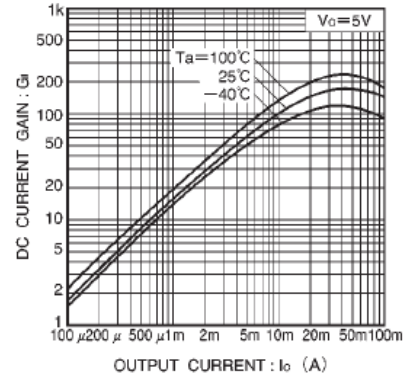


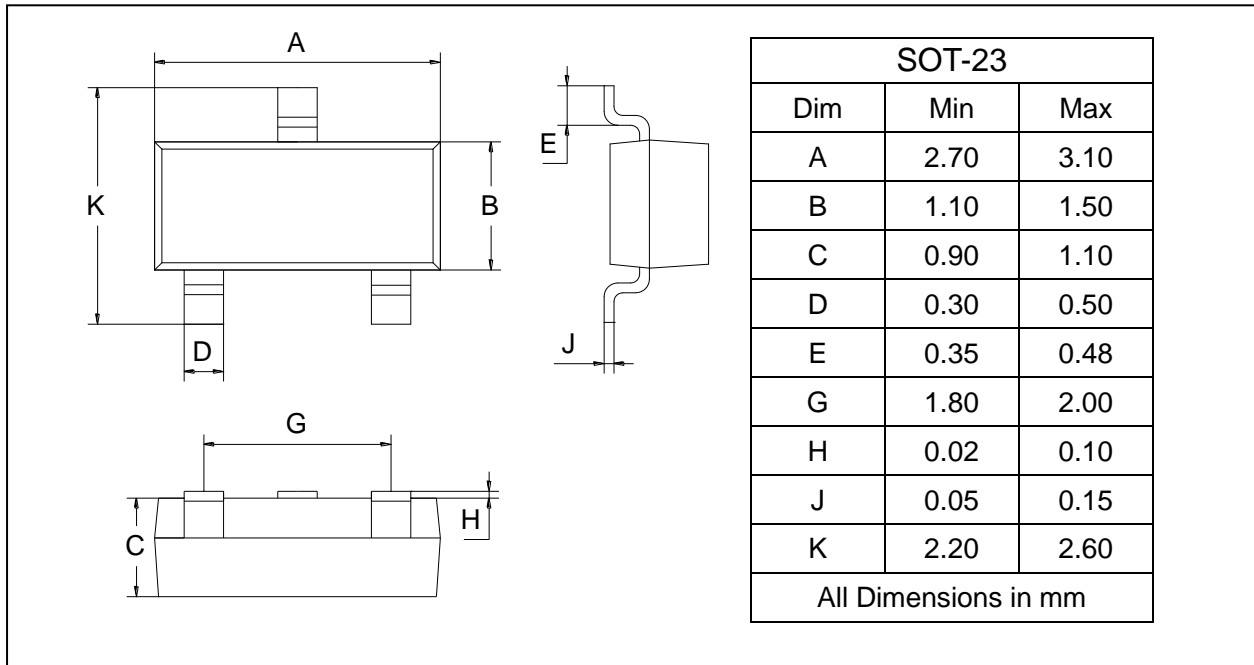
Fig.3 DC current gain vs. output current



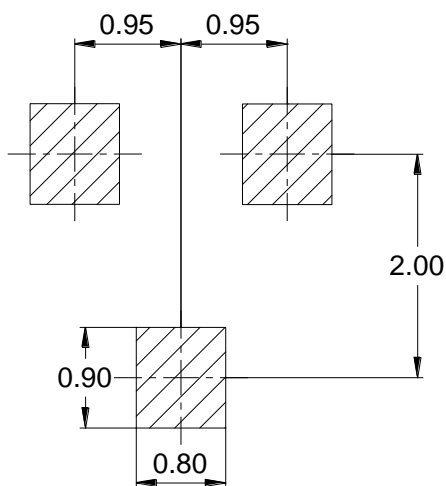
### PACKAGE OUTLINE

Plastic surface mounted package

SOT-23



### SOLDERING FOOTPRINT



### PACKAGE INFORMATION

Device	Package	Shipping
DTC114ECA/124ECA/143ECA/144ECA/115ECA	SOT-23	3000 pcs / Tape & Reel