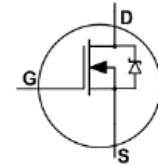




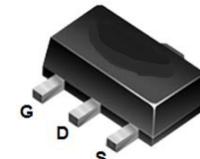
Features

- Depletion mode (Normally On)
- Proprietary advanced planar technology
- Low leakage Current



Applications

- Transient protect
- Converters



Mechanical Data

- Case: SOT-89
- Molding Compound: UL Flammability Classification Rating 94V-0
- Terminals: Matte tin-plated leads; solderability-per MIL-STD-202, Method 208

SOT-89

Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
LGE4022	SOT-89	1000 pcs / Tape & Reel	4022

Maximum Ratings (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage ^{*1}	V_{DSX}	400	V
Drain-to-Gate Voltage ^{*1}	V_{DGX}	400	V
Gate-to-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	0.2	A
Pulsed Drain Current ^{*2}	I_{DM}	0.8	A
Gate Source ESD ^{*3}	V_{ESD}	3000	V
Source to Gate ESD ^{*3}		3000	V

Thermal Characteristics

Parameter	Symbol	Value	Unit
Power Dissipation	P_D	1	W
Thermal Resistance Junction-to-Case	$R_{\theta JC}$	125	$^\circ\text{C}/\text{W}$
Operating Junction Temperature Range	T_J	-55 ~ +150	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-55 ~ +150	$^\circ\text{C}$


Electrical Characteristics (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Off Characteristics						
BV_{DSX}	Drain-to-Source Breakdown Voltage	$V_{\text{GS}} = -5\text{V}$, $I_D = 250\mu\text{A}$	400	-	-	V
$I_{\text{D(OFF)}}$	Drain-to-Source Leakage Current	$V_{\text{DS}} = 400\text{V}$, $V_{\text{GS}} = -5\text{V}$	-	-	1	μA
		$V_{\text{DS}} = 400\text{V}$, $V_{\text{GS}} = -5\text{V}$, $T_J = 125^\circ\text{C}$	-	-	1	mA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}} = \pm 20\text{V}$, $V_{\text{DS}} = 0\text{V}$	-	-	± 5	μA
On Characteristics *4						
I_{DSS}	Saturated Drain-to-Source Current	$V_{\text{GS}} = 0\text{V}$, $V_{\text{DS}} = 25\text{V}$	0.2	-	-	A
$R_{\text{DS(ON)}}$	Static Drain-Source On-resistance	$V_{\text{GS}} = 10\text{V}$, $I_D = 200\text{mA}$	-	-	23	Ω
		$V_{\text{GS}} = 0\text{V}$, $I_D = 200\text{mA}$	-	-	25	Ω
$V_{\text{GS(OFF)}}$	Gate-to-Source Cut-off Voltage	$V_{\text{DS}} = 3\text{V}$, $I_D = 8\mu\text{A}$	-3.3	-	-1.5	V
g_{fs}	Forward Transconductance	$V_{\text{DS}} = 10\text{V}$, $I_D = 200\text{mA}$	-	193	-	mS
Dynamic Characteristics						
C_{ISS}	Input Capacitance	$V_{\text{GS}} = -5\text{V}$ $V_{\text{DS}} = 25\text{V}$ $f = 1.0\text{MHz}$	-	103.2	-	pF
C_{OSS}	Output Capacitance		-	17.7	-	
C_{RSS}	Reverse Transfer Capacitance		-	5.2	-	
Switching Characteristics						
$t_{\text{d(ON)}}$	Turn-on Delay Time	$V_{\text{DD}} = 100\text{V}$ $V_{\text{GS}} = -5\text{V} \sim 5\text{V}$ $R_G = 10\Omega$ $I_D = 200\text{mA}$	-	6.6	-	ns
t_r	Turn-on Rise Time		-	9.2	-	
$t_{\text{d(OFF)}}$	Turn-Off Delay Time		-	18.8	-	
t_f	Turn-Off Fall Time		-	356	-	
Q_G	Total Gate-Charge	$V_{\text{DD}} = 100\text{V}$ $V_{\text{GS}} = -5\text{V} \sim 5\text{V}$ $I_D = 200\text{mA}$	-	359.6	-	nC
Q_{GS}	Gate to Source Charge		-	61.6	-	
Q_{GD}	Gate to Drain (Miller) Charge		-	130	-	
Source-Drain Diode Characteristics						
V_{SD}	Diode Forward Voltage *4	$I_{\text{SD}} = 200\text{mA}$, $V_{\text{GS}} = -10\text{V}$	-	-	1.5	V

Notes:

1. $T_J = 25 \sim 150^\circ\text{C}$
2. Repetitive rating, pulse width limited by maximum junction temperature
3. The test is based on JEDEC EIA/JESD22-A114(HBM)
4. Pulse width $\leq 380\mu\text{s}$; duty cycle $\leq 2\%$



Ratings and Characteristics Curves (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

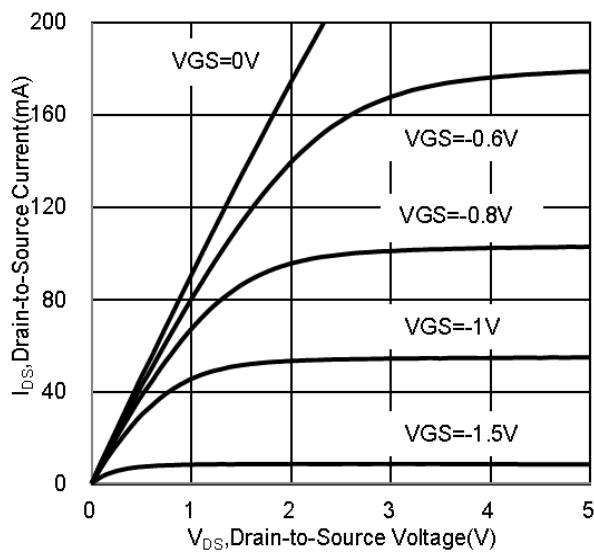


Fig 1 On-Region Characteristics

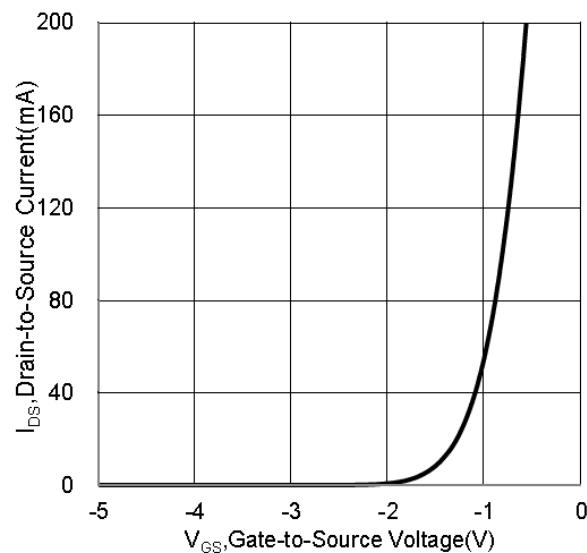


Fig 2 Transfer Characteristics

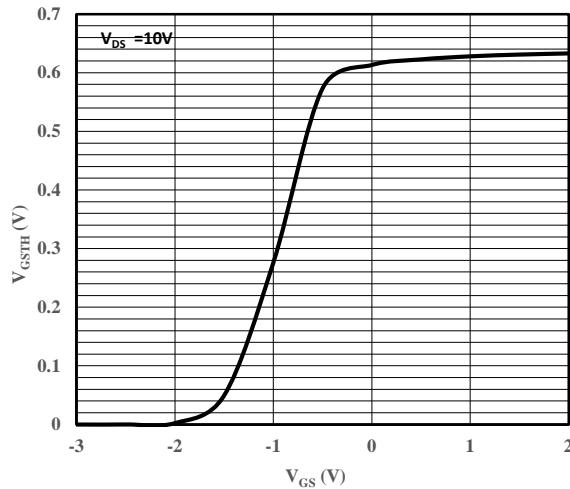


Fig 3 Transfer Characteristics

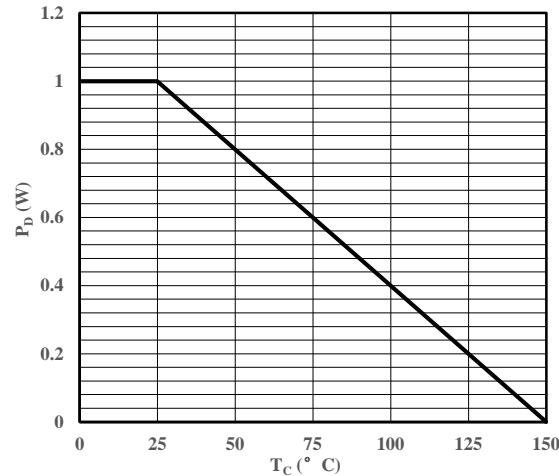
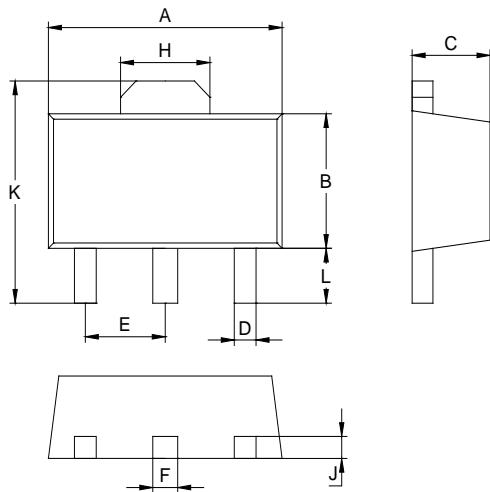


Fig 4 Power dissipation vs temp.



Package Outline Dimensions (Unit: mm)



SOT-89		
Dimension	Min.	Max.
A	4.30	4.70
B	2.25	2.65
C	1.30	1.70
D	0.30	0.50
E	1.40	1.60
F	0.38	0.58
H	1.60	1.80
J	0.30	0.50
L	0.90	1.10
K	3.95	4.35

Mounting Pad Layout (Unit: mm)

SOT-89

